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Date of Mailing: January 12, 2005

Our Case No.: 10808/201  
IN1239WOUS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

GEORG TEMPEL

Serial No.:

Filing Date: January 11, 2005

For: SEMICONDUCTOR COMPONENT  
WITH STRESS-ABSORBING  
SEMICONDUCTOR LAYER AND  
ASSOCIATED FABRICATION  
METHOD

Examiner: Not Assigned

Group Art Unit No.: Not Assigned

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on the attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. § 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

This Information Disclosure Statement is being filed prior to the receipt of the first Official Action reflecting an examination on the merits and within three months of the filing date. This Information Disclosure Statement is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). Hence, no fees are believed to be due in connection with filing of this Information Disclosure Statement. However, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these material, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925. A duplicate copy of this document is enclosed.

Applicants respectfully request that the listed documents be made of record in the present case.

Respectfully submitted,



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Stephen Charles Smith  
Registration No. 53,617  
Attorney for Applicants

BRINKS HOFER GILSON & LIONE  
P.O. Box 10395  
Chicago, IL 60610  
(312) 321-4200

FORM PTO-1449		SERIAL NO. Not Assigned	CASE NO. 10808/201
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)		FILING DATE January 12, 2004	GROUP ART UNIT 321498 Not Assigned
		APPLICANT(S): Georg Tempel	

## REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
A1	5,810,924	9/22/1998	Legoues et al.		6/7/1995
A2	5,569,538	10/29/1996	Cho		10/14/1994
A3	5,232,862	8/3/1993	Heremans et al.		1/13/1992
A4					
A5					
A6					
A7					
A8					
A9					
A10					
A11					
A12					
A13					

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER Number-Kind Code (if known)	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
A14	EP 1 120 818 A1	8/1/2001	EPO		No
A15	11233440	8/27/1999	EPO		Yes
A16					
A17					
A18					

EXAMINER INITIAL	OTHER ART - NON PATENT LITERATURE DOCUMENTS	
	(Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)	
A19	Tsutomu Tezuka, Naoharu Sugiyama, Tomohisa Mizuno and Shin-ichi Takagi "High Performance Strained Si-on-Insulator MOSFETs by Novel Fabrication Processes Utilizing Ge-Condensation Technique" 2002 Symposium on VLSI Technology Digest of Technical Papers, pp. 96-97	
A20	T. Tezuka; N. Sugiyama, S. Takagi "Fabrication of a Strained Si on sub-10-nm-thick SiGe-on-insulator virtual substrate" Materials and Science Engineering B89, 2002, pp. 360-363	
A21	D.L. Harame, J.H. Comfort, J.D. Cressler, E.F. Crabbé, J.Y.-C Sun, B.S. Meyerson, T. Tice "Si/SiGe Epitaxial-Base Transistors-Part II: Process Integration and Analog Applications" IEEE Transactions on Electron Devices, Volume 42, No. 3, March 1995, pp. 469-482	

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609;  
 Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449	SERIAL NO. Not Assigned	CASE NO. 10808/201
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE January 12, 2004	GROUP ART UNIT 10/521498 Note: Not assigned
	APPLICANT(S): Georg Tempel	

EXAMINER INITIAL		OTHER ART – NON PATENT LITERATURE DOCUMENTS (Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)
	A22	H. Zogg "Strain Relief in Epitaxial Fluoride Buffer Layers for Semiconductor Heteroepitaxy" Applied Physics Letters, No. 15, October 13, 1986, pp. 933-935
	A23	
	A24	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.